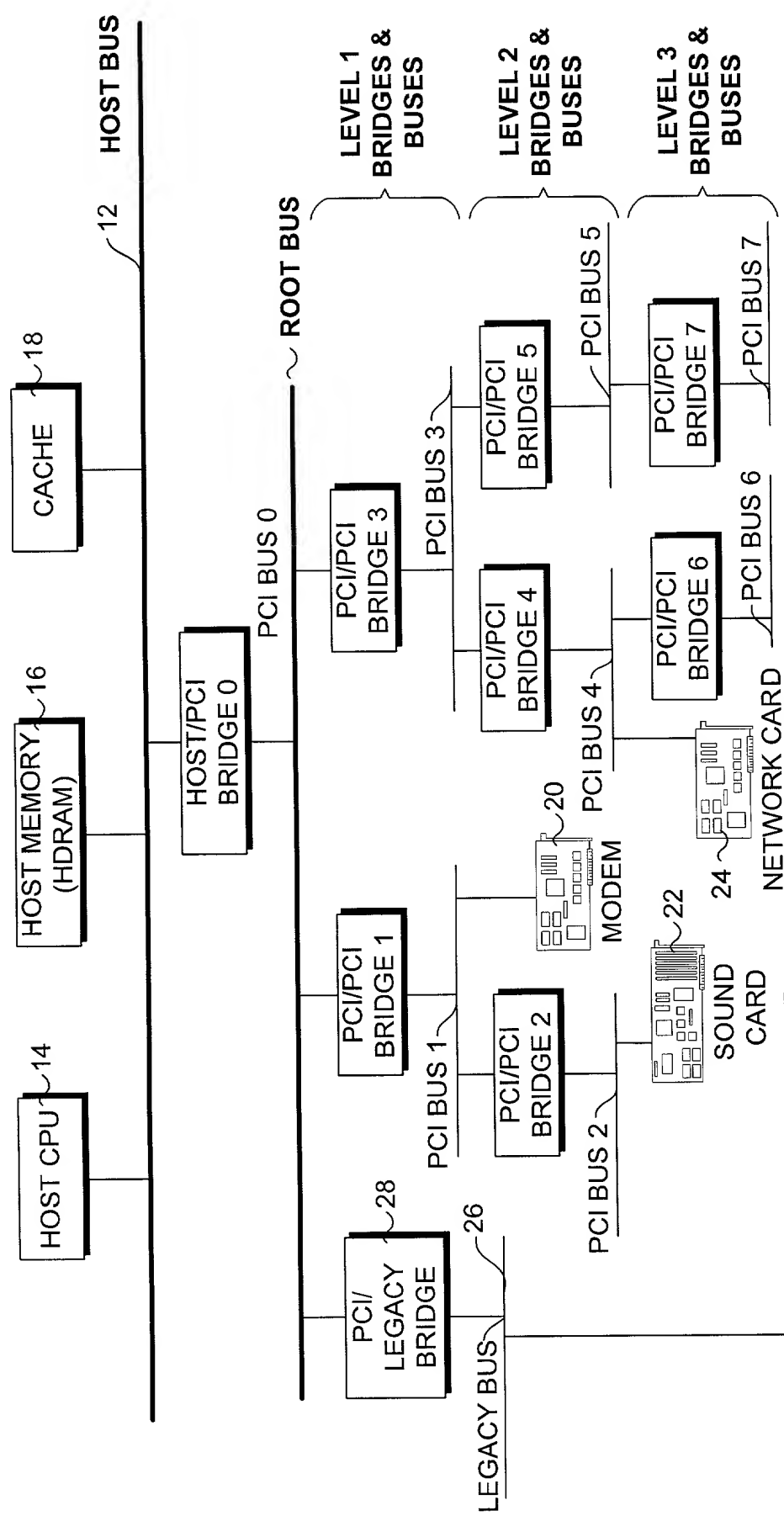
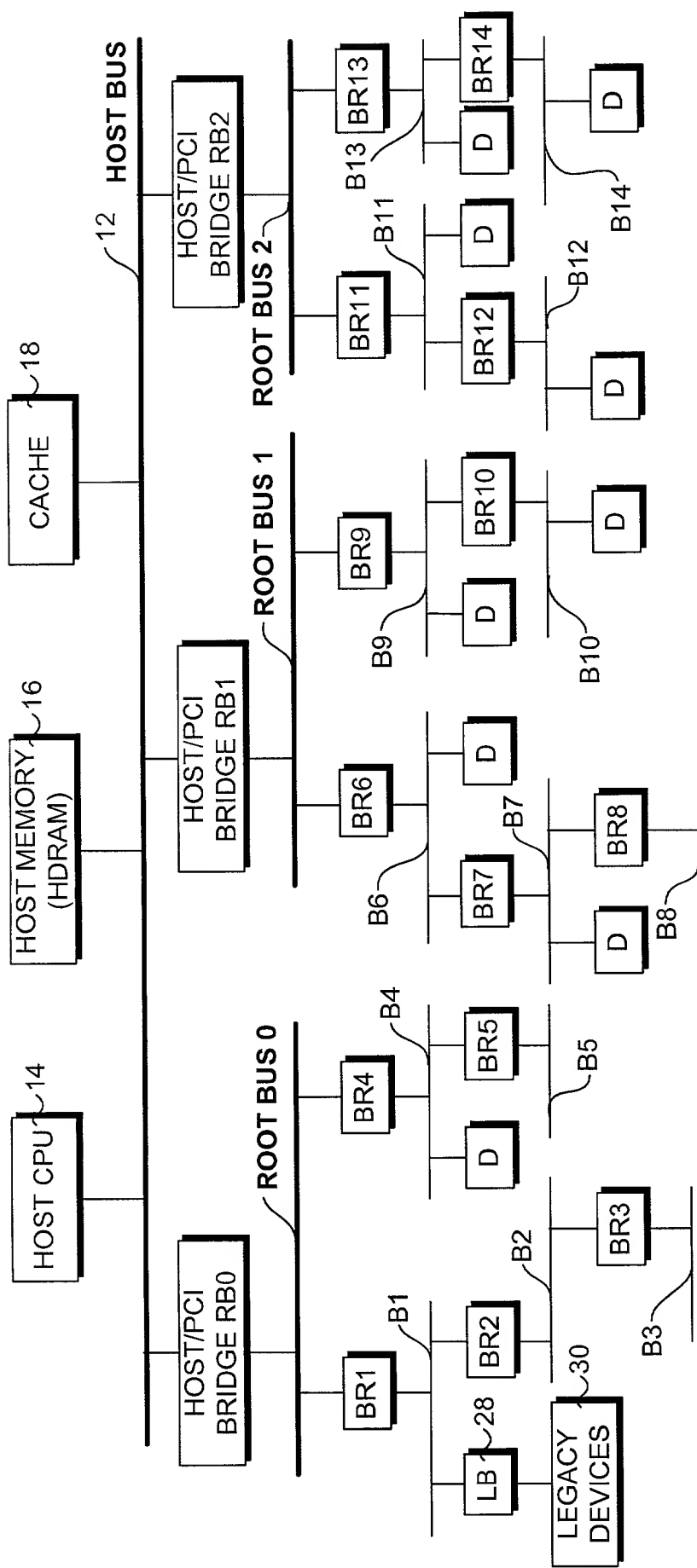


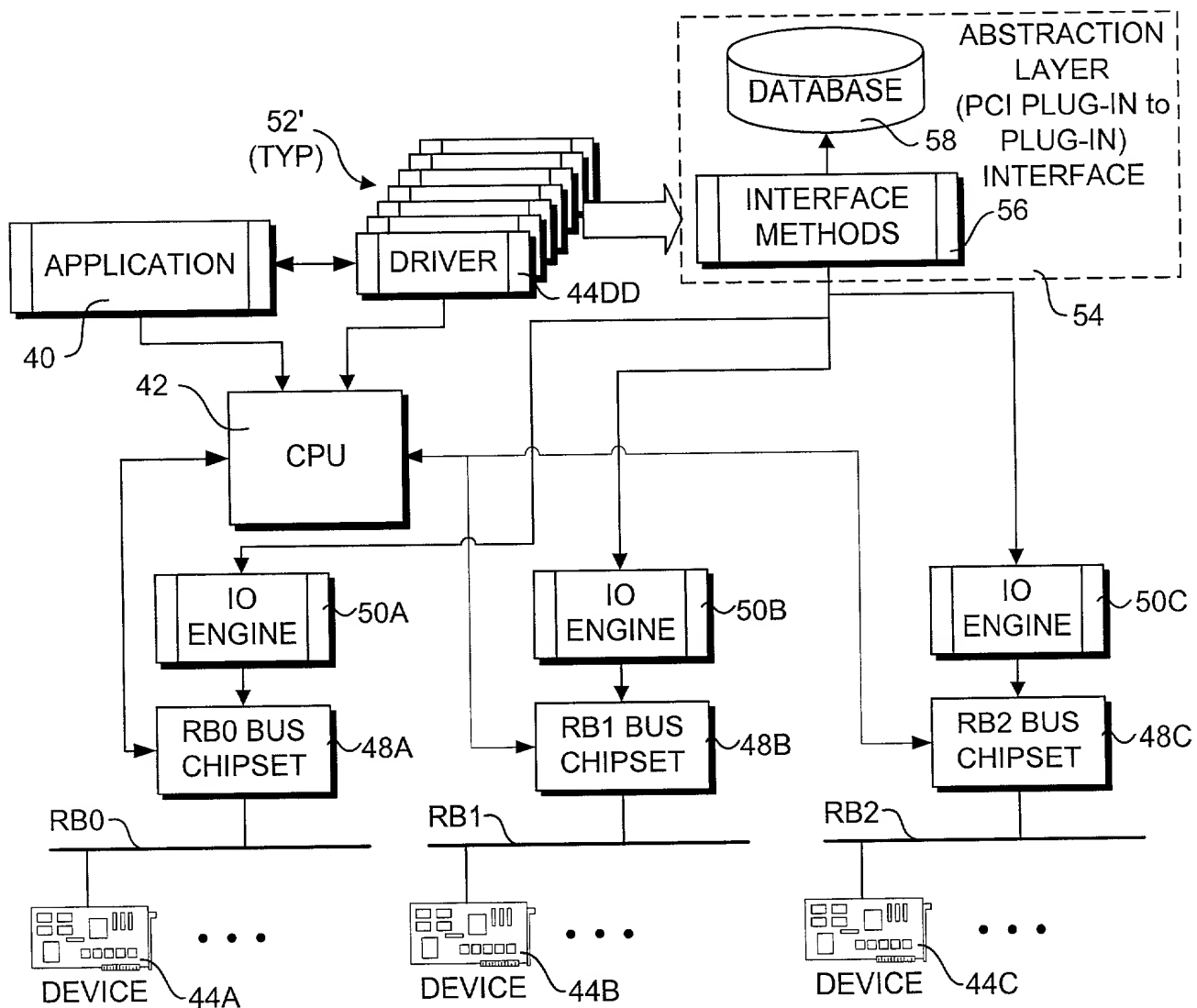
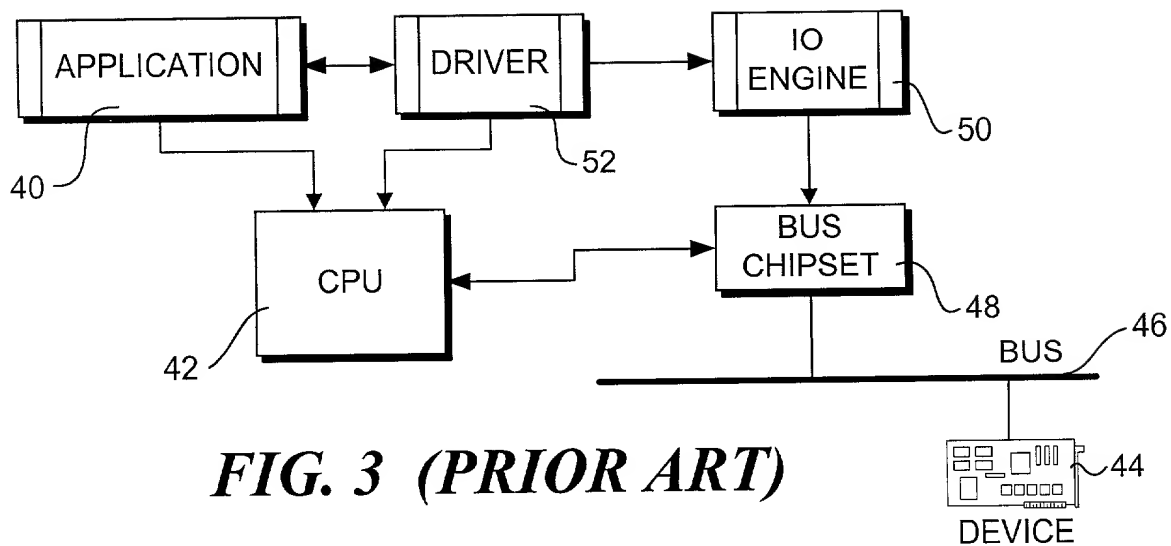
FIG. 1 is a block diagram of a computer system architecture showing a hierarchical bus structure. The system includes a Host CPU (14), Host Memory (HDRAM) (16), and Cache (18) connected to a Host Bus (12). A Host/PCI Bridge (0) connects the Host Bus to a Root Bus. The Root Bus is a PCI BUS 0, which branches into three levels of bridges and buses. Level 1 includes PCI/PCI Bridge 1, PCI/PCI Bridge 2, and PCI/PCI Bridge 3. Level 2 includes PCI/PCI Bridge 4, PCI/PCI Bridge 5, and PCI/PCI Bridge 6. Level 3 includes PCI/PCI Bridge 7. The Root Bus also connects to a Legacy Bus (26) via a PCI/Legacy Bridge (28). The Legacy Bus connects to Legacy Compatible Resources (30), which include DMA Controller, KBRD, MOUSE & VIDEO, FLOPPY & HARD DISK, and RTC & TIMER. The Root Bus also connects to a Network Card (24) and a Sound Card (22). A Modem (20) is connected to PCI BUS 2. The Root Bus is labeled as LEVEL 1 BRIDGES & BUSES, LEVEL 2 BRIDGES & BUSES, and LEVEL 3 BRIDGES & BUSES.

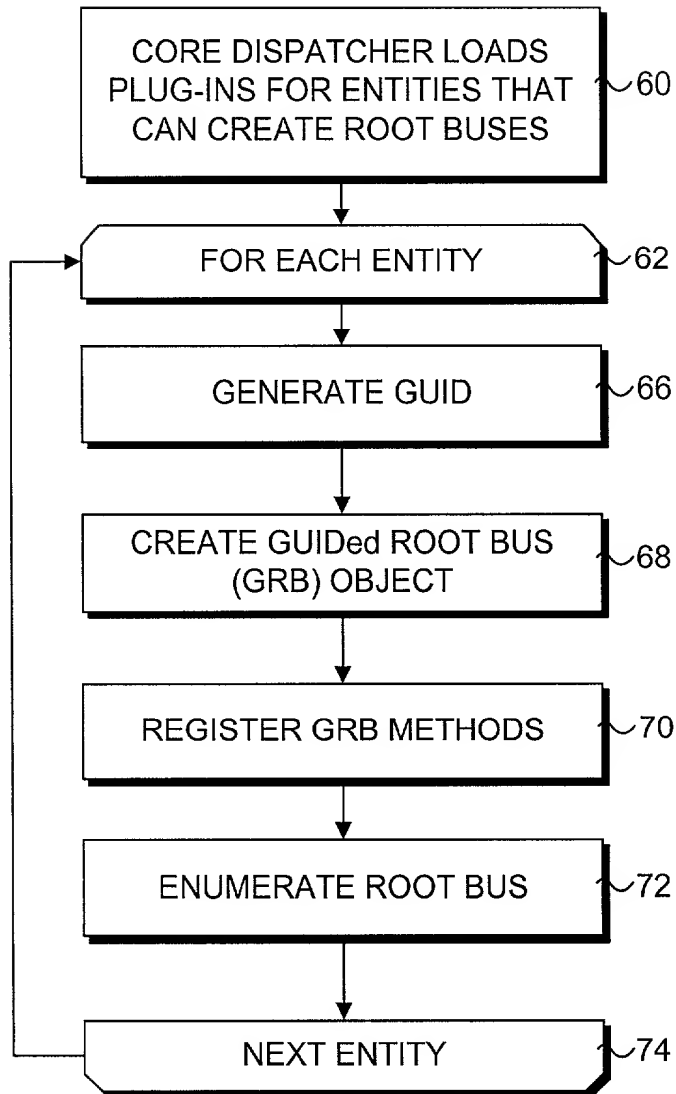


**FIG. 1**



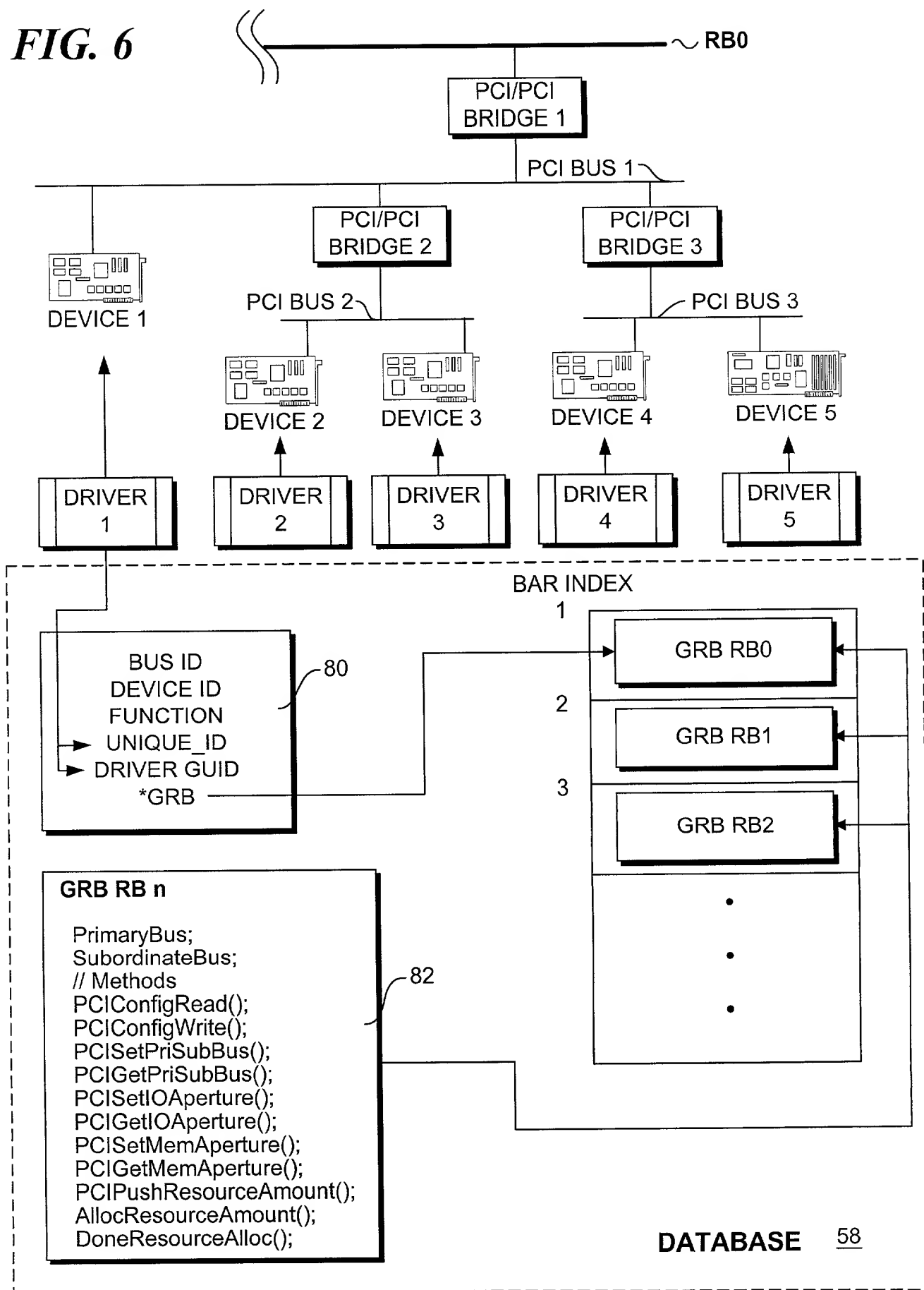
**FIG. 2**

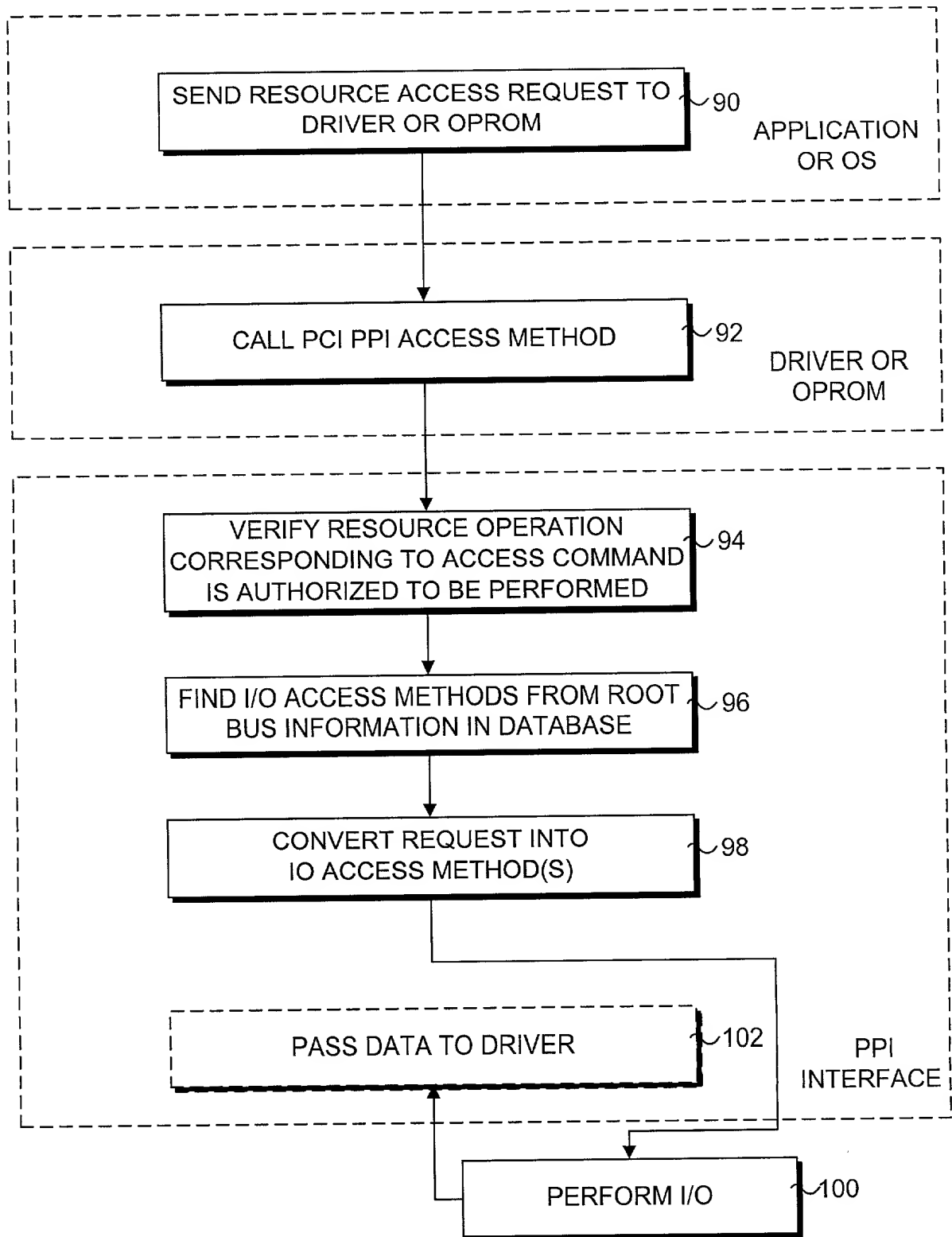




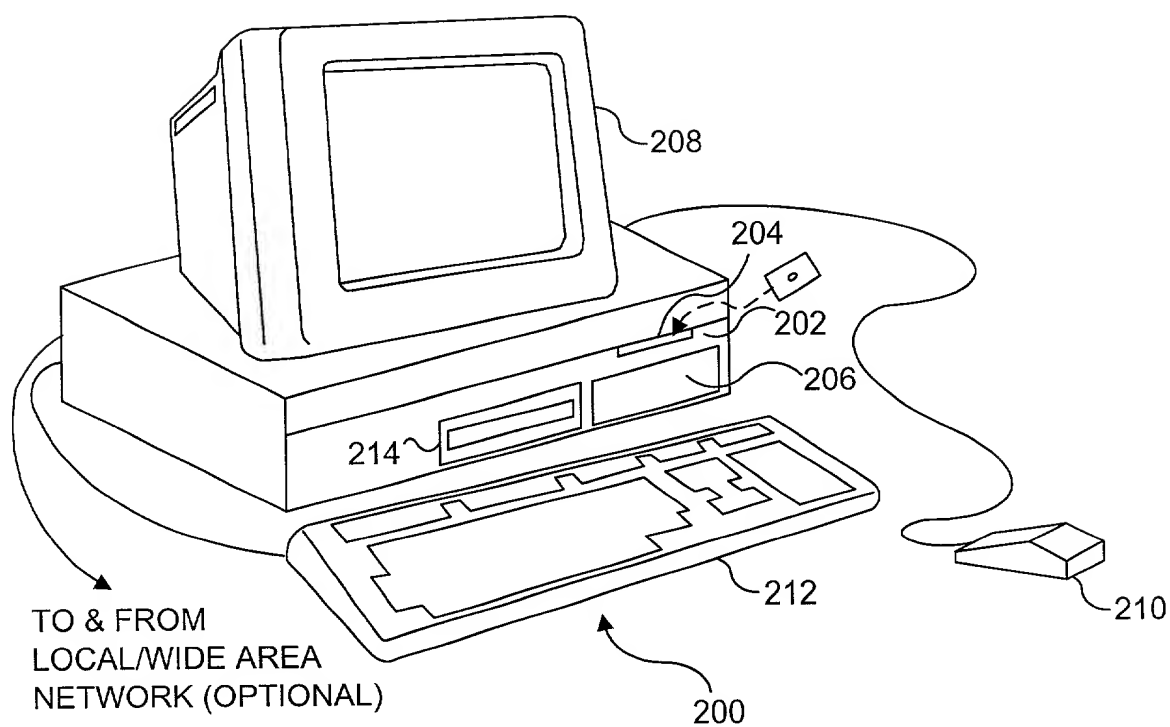
**FIG. 5**

**FIG. 6**





**FIG. 7**



**FIG. 8**